

Notice of References Cited	Application/Control No. 09/759,603	Applicant(s)/Patent Under Reexamination DRESCHER ET AL.	
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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,208,954 B1	03-2001	Houtchens, Steven P.	703/16
	B	US-6,262,544 B1	07-2001	Disser et al.	318/245
	C	US-4,300,205	11-1981	Tansuwan, Chusak	703/8
	D	US-4,463,605	08-1984	McDowell et al.	73/866.4
	E	US-6,438,462	08-2002	Hanf et al.	700/297
	F	US-5,479,355	12-1995	Hyduke, Stanley M.	703/14
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Becerra et al., "Four-Quadrant Brushless ECM Drive with Integrated Current Regulation" IEEE. 1992. pg. 833-841.
	V	Bachmayer et al., "An Accurate Four-Quadrant Nonlinear Dynamical Model for Marine Thrusters: Theory and Experimental Validation" 2000. pg. 146-159.
	W	Walker-J., "Four Quadrant Amplifier" 1995 IEEE pg. 947-951.
	X	Veijola et al., "Dynamic Modelling and Simulation of Microelectromechanical Devices with a Circuit Simulation Program" 1998 Helsink Univ. of Technolgy. pg.245-250.

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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	A	US-			
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	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
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	M	US-			

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	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wilamowski-B., "VLSI Analog Multiplier/Divider Circuit" 1998 IEEE pg. 493-496.
	V	Fedder et al., "NODAS 1.3--Nodal Design of Actuators and Sensors" Carnegie Mellon Univ. 1998 pg. 1-8.
	W	
	X	

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Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.